

Docket No. 271254US2X PCT



JC04 Rec'd PCT/PTO 19 JUL 2005  
NO STOP PCT

#4

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Samuel BOUTIN, et al.

SERIAL NO: 10/532,089

GAU:

FILED: April 21, 2005

EXAMINER:

FOR: METHOD AND DEVICE FOR SYNTHESISING AN ELECTRICAL ARCHITECTURE

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

COMMISSIONER FOR PATENTS  
ALEXANDRIA, VIRGINIA 22313

SIR:

Applicant(s) wish to disclose the following information.

REFERENCES

- ☒ The applicant(s) wish to make of record the references listed on the attached form PTO-1449. Copies of the listed references are attached, where required, as are either statements of relevancy or any readily available English translations of pertinent portions of any non-English language references.
- ☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

RELATED CASES

- ☐ Attached is a list of applicant's pending application(s), published application(s) or issued patent(s) which may be related to the present application. In accordance with the waiver of 37 CFR 1.98 dated September 21, 2004, copies of the cited pending applications are not provided. Cited published and/or issued patents, if any, are listed on the attached PTO form 1449.
- ☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

CERTIFICATION

- ☐ Each item of information contained in this information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.
- ☐ No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

DEPOSIT ACCOUNT

- ☒ Please charge any additional fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit account number 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,  
MAIER & NEUSTADT, P.C.

Marvin J. Spivak

Registration No. 24,913

Customer Number

22850

Tel. (703) 413-3000  
Fax. (703) 413-2220  
(OSMMN 05/03)

Surinder Sachar

Registration No. 34,423

Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. 271254US2X PCT		SERIAL NO. 10/532,089	
LIST OF REFERENCES CITED BY APPLICANT				APPLICANT Samuel BOUTIN, et al.			
				FILING DATE April 21, 2005		GROUP	
<b>U.S. PATENT DOCUMENTS</b>							
EXAMINER INITIAL	AA	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA	6,457,165	09/24/02	ISHIKAWA, Takeshi et al.			
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						
<b>FOREIGN PATENT DOCUMENTS</b>							
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES                  NO		
	AO	2 775 806	09/10/99	FR(with English abstract)		NO	
	AP	0 696 775	02/14/96	EP		NO	
	AQ	02/059801	08/01/02	WO		NO	
	AR						
	AS						
	AT						
	AU						
	AV						
<b>OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)</b>							
	AW	GIUSTO, Paolo et al.: "Automotive Virtual Integration Platforms: Why's, What's, and How's" Proceedings of the 2002 IEEE International Conference on Computer Design: VLSI in Computers and Processors (ICCD'02), September 16-18, 2002. XP010619768					
	AX	SCHLOSSER, Joachim: "Institute of Electrical and Electronics Engineers: "Requirements for Automotive System Engineering Tools", Proceedings 2002 IEEE International Conference on Computer Design: VLSI in Computers and Processors. ICCD' 2002, pages 1-6, September 16-18, 2002. XP10619767					
	AY	FUCHS, Max et al.: "Advanced Design and Validation Techniques for Electronic Control Units", SAE Paper 980199, pages 1-6, 1998. XP-002204717					
	AZ	BORTOLAZZI, Juergen et al.: "Specification and Design of Electronic Control Units", Proceedings of Euro-Dac '96: European Design Automation Conference with EURO-DVL '96 and Exhibition, pages 36-41, September 16, 1996. XP010198661				<input type="checkbox"/> Additional References sheet(s) attached	
Examiner					Date Considered		
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

U.S. PCT Application Serial No.: 10/532,089  
Docket No.: 271254US2X PCT

#### STATEMENT OF RELEVANCY

- 1) References AO and AW-AZ have been cited in the International Search Report. Copies of these references are being submitted herewith only when not automatically provided by the International Searching Authority.
- 2) References AA, AP and AQ have been cited in the corresponding French Search Report. A copy of these references is being submitted herewith.
- 3) Reference \_\_\_\_ is discussed in the specification. A copy of these references is being submitted here with.
- 4) Reference \_\_\_\_\_ is additional prior art known to Applicant. A copy of these references is being submitted herewith.